

a second inductor coupled between the third terminal of the second transistor and the common node to reduce the noise of the current mirror.

16. (Amended once) An amplifier cell comprising:

first and second input terminals;

first and second output terminals;

first class AB input stage coupled to the first and second output terminals and arranged to drive the first and second output terminals responsive to a first input signal received at the first input terminal; and

a second class AB input stage coupled to the first and second output terminals and arranged to drive the first and second output terminals responsive to a second input signal received at the second input terminal;

wherein the first class AB input stage comprises:

a first transistor having a first terminal coupled to the first output terminal, a second terminal coupled to receive a bias signal, and a third terminal coupled to receive the first input signal; and

a first current mirror coupled between the first input terminal and the second output terminal.

22. (Amended once) An amplifier cell comprising:

first and second input terminals;

first and second output terminals;

first class AB input stage coupled to the first and second output terminals and arranged to drive the first and second output terminals responsive to a first input signal received at the first input terminal; and

a second class AB input stage coupled to the first and second output terminals and arranged to drive the first and second output terminals responsive to a second input signal received at the second input terminal;

wherein each of the class AB input stages comprises:

a common base transistor coupled between a first one of the input terminals;

an inductor coupled between the common base transistor and a first one of the output terminals; and

an inductively degenerated current mirror coupled between the first one of the input terminals and the other output terminal.